



system inter-connection(OSI) layer designs; a physical layer, a data link layer and an application layer.

### 3. Experimental results

In this paper, we focus on the NIC's operability, ignoring all the issues related to the management functions and the control functions. Therefore, the algorithms related to a routing, congestion control, retransmission mechanism and resources management have not been implemented.

The PLD-based prototype of NIC is embedded as a module into standard 3U and 19 inch rack-mount enclosures. Through the transmission test between two nodes, the transmission delay and the frame loss rate are measured to ensure the operability of the implemented NIC. To measure the transmission delay, two nodes are connected via a single fiber optic cable as shown in Figure 3 and the details of the test environments are shown in Table I.

Table I. The test environments

| Component                 | Specification  |
|---------------------------|--|
| Host PC                   | - Intel Pentium III processor, 256 memory<br>- OS : Windows XP Pro<br>- TI(Texas Instrument) Code composer<br>- TMS320C40 Assembler, Linker & Debugger |
| Source & Destination node | - VME bus backplane<br>- Standard 3U & 19 inch rack-mount<br>- TMS320C40 DSP<br>- Network Interface Card<br>- Power supply                             |
| Fiber optic cable         | - 1350nm Single mode<br>- ST type connector  |

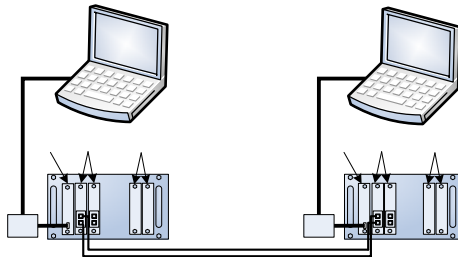


Fig. 3. Development & test environments

Code composer is the DSP industry's first fully integrated development environment (IDE) with DSP-specific functionality. The DSP programs are written in Assembler in order to achieve high speed real-time processing. The application program, programmed as a test program at the Host PC, is written in Assembler and is downloaded to the DSP via a JTAG emulator. The JTAG emulator transmits data between the host PC and the DSP board and accesses the state of the DSP's memory in a real time.

#### 3.1 Transmission delay

The source node transmits a frame to the destination node periodically by a scheduler for 24 hour. In most NPP I&C systems, the data length is about 1 ~ 128 bytes at regular intervals. Therefore we designed the

frame structure so that it has 128 byte data in the data field.

The transmission delay includes the processing delay of the NIC and the propagation delay of a fiber optic cable. The propagation delay between two nodes measured by an oscilloscope is a minimum of 183  $\mu$ s and a maximum of 185  $\mu$ s. The overall delay between two nodes, including the API processing delays, is a minimum of 1,472  $\mu$ s and a maximum of 1,476  $\mu$ s, namely it is within a finite, predictable time delay, a timely arrival of a transmitted frame from the source node to the destination node can be acquired at all times.

#### 3.2 Frame loss rate

The frame loss rate, an important characteristic of the NIC's performance, is measured between two nodes for 24 hours. The source node transmits frames and the destination node receives them for 24 hours and counts the total number of transmitted frames and received frames respectively. Because the scheduler calls on the NIC API periodically at 25ms, about 3,456,000 frames are transmitted and received in 24 hours. The frame loss rate of this test is 0%, namely, all the transmitted frames at the source node are received at the destination node.

### 4. Conclusions

The PLD-based prototype of our NIC for the safety functions of safety systems has been implemented and tested. Through the transmission tests, the measured transmission delay of the NIC was within a finite, predictable time delay. Also during 24 hours' transmission tests, the frame loss rate was 0%, namely, the intended functions of the NIC were achieved. Through these activities, the operability and the intended functions of the NIC were tested and confirmed. Further demonstration of the performance and functions of the NIC under various test environments remain as future works.

### REFERENCES

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